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Peter Scheidler

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EXPERIENCE

Senior Electrical Engineer

Bruker

March 2015-Present

Responsible for hardware and firmware design and implementation of mass spectroscopy systems.

Director of Electrical Engineering

Pyramid Technical Consultants

2006-March 2015

Responsible for leading teams through all areas of development and implementation for products to measure dose delivered for cancer treatment and semiconductor manufacturing.

Helped the company growth from 5 to 20 people and 2 to 30 products.

Electrical Engineer

A2e Technologies

2005-2006

Responsible for FPGA design as a consultant to multiple companies, with mostly on site work and direct customer interaction

Hardware Engineer

L-3 Security and Detection Systems

2002-2005

Responsible for hardware and firmware design of security x-ray systems.

Hardware Engineer

RadiSys

2000-2002

Responsible for hardware and firmware design of voice over IP cards.

EDUCATION

Harvey Mudd College, Claremont, CA.

2000

Bachelor of Science in Engineering

Boston University, Boston, MA.

2014

Graduate Certificate in Project Management

CERTIFICATION

Project Management Professional (PMP) Certification

2014-Present